

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	15752	(plural\$3 near3 delay\$3 or multi\$3 near3 delay\$3 or plural\$3 nar3 DLL or multi\$3 near3 DLL or plural\$3 near3 DPLL or multi\$3 near3 DPLL) same (add\$3 or summ\$3 or combin\$3 or compar\$6) same (transition\$1 near2 tim\$3 or tim\$3 near2 control\$4 or control\$4 adj1 signal\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/07/30 12:59	
2	BRS	L2	5575	(plural\$3 near3 delay\$3 or multi\$3 near3 delay\$3 or plural\$3 nar3 DLL or multi\$3 near3 DLL or plural\$3 near3 DPLL or multi\$3 near3 DPLL) with (add\$3 or summ\$3 or combin\$3 or compar\$6) with (transition\$1 near2 tim\$3 or tim\$3 near2 control\$4 or control\$4 adj1 signal\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/07/30 13:00	
3	BRS	L3	203	(plural\$3 adj3 delay\$3 or multi\$3 adj3 delay\$3 or plural\$3 adj3 DLL or multi\$3 adj3 DLL or plural\$3 adj3 DPLL or multi\$3 adj3 DPLL) with (add\$3 or summ\$3 or combin\$3 or compar\$6) with (transition\$1 near2 tim\$3 or tim\$3 near2 control\$4 or control\$4 adj1 signal\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/07/30 13:01	

	Error Definition	Er ro rs
1		0
2		0
3		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
4	BRS	L4	43	(plural\$3 adj3 delay\$3 or multi\$3 adj3 delay\$3 or plural\$3 adj3 DLL or multi\$3 adj3 DLL or plural\$3 adj3 DPLL or multi\$3 adj3 DPLL) with (combin\$3) with (transition\$1 near2 tim\$3 or tim\$3 near2 control\$4 or control\$4 adj1 signal\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/07/3 0 13:19	
5	BRS	L5	25	(plural\$3 adj3 delay\$3 or multi\$3 adj3 delay\$3 or plural\$3 adj3 DLL or multi\$3 adj3 DLL or plural\$3 adj3 DPLL or multi\$3 adj3 DPLL) with (add\$3 near4 delay\$3 or summ\$3 near4 delay\$3) with (transition\$1 near2 tim\$3 or tim\$3 near2 control\$4 or control\$4 adj1 signal\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/07/3 0 13:20	

	Error Definition	Er ro rs
4		0
5		0